

# WEST Search History

Hide Items

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DATE: Thursday, June 03, 2004

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L17	('6128758')!.PN.	2
		ram near(block or plural\$4 or group or second) same (module or parallel)same	
<input type="checkbox"/>	L16	(lead or pad\$4 or pin or terminal or port or inter\$1port or bus or interfac\$4 or 'i/o' or 'io' or input or output or access\$3 or inter\$1connec\$5 or net) near test	13
<input type="checkbox"/>	L15	L10 and test bus	2
<input type="checkbox"/>	L14	L11 and test with bus	4
<input type="checkbox"/>	L13	('US20030009712A')!.PN.	0
<input type="checkbox"/>	L12	('20030009712')!.PN.	2
<input type="checkbox"/>	L11	ram block same module same test\$3 and L10	10
<input type="checkbox"/>	L10	ram block same module	77
<input type="checkbox"/>	L9	L8 and (dedicat\$3 or special)near bus same test\$3 and (tap or jtag) and ram	6
<input type="checkbox"/>	L8	L6 and (dedicat\$3 or special)near bus same test\$3 and (tap or jtag)	16
<input type="checkbox"/>	L7	(dedicat\$3 or special)near bus same test\$3 same ram	1
<input type="checkbox"/>	L6	(dedicat\$3 or special)near bus same test\$3	107
<input type="checkbox"/>	L5	('5423050')!.PN.	2
<input type="checkbox"/>	L4	L3 and jtag and test bus with (ram or rom or memory)same compar\$4 same (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux)	1
<input type="checkbox"/>	L3	L2 and compar\$4 and (switch\$3 or select\$3 or multiplex\$3 or mux or de\$1multiplex\$3 or de\$1mux)	153
<input type="checkbox"/>	L2	L1 and jtag and (ram or rom or memory)	174
<input type="checkbox"/>	L1	test bus	1339

END OF SEARCH HISTORY

## WEST Search History





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Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L12	('6128758')!.PN.	2
		ram near(block or plural\$4 or group or second) same (module or parallel)same	
<input type="checkbox"/>	L11	(lead or pad\$4 or pin or terminal or port or inter\$1port or bus or interfac\$4 or 'i/o' or 'io' or input or output or access\$3 or inter\$1connec\$5 or net) near test	13
<input type="checkbox"/>	L10	L5 and test bus	2
<input type="checkbox"/>	L9	L6 and test with bus	4
<input type="checkbox"/>	L8	('US20030009712A')!.PN.	0
<input type="checkbox"/>	L7	('20030009712')!.PN.	2
<input type="checkbox"/>	L6	ram block same module same test\$3 and L5	10
<input type="checkbox"/>	L5	ram block same module	77
<input type="checkbox"/>	L4	l3 and (dedicat\$3 or special)near bus same test\$3 and (tap or jtag) and ram	6
<input type="checkbox"/>	L3	l1 and (dedicat\$3 or special)near bus same test\$3 and (tap or jtag)	16
<input type="checkbox"/>	L2	(dedicat\$3 or special)near bus same test\$3 same ram	1
<input type="checkbox"/>	L1	(dedicat\$3 or special)near bus same test\$3	107

END OF SEARCH HISTORY